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JC901 U.S. PTO
09/634750
08/09/00

DARBY & DARBY P.C.

805 Third Avenue
New York, New York 10022
212-527-7700

Docket No: 1073/OH510

Box PATENT APPLICATION
Assistant Commissioner for Patents
Washington, DC 20231

Sir:

Enclosed please find an application for United States patent as identified below:

Inventor/s (name ALL inventors): Miguel Philipe Paul PEETERS

Title: POWER REDUCTION

including the items indicated:

1. Specification and 20 claims: 4 indep.; 16 dep.; X multiple dep.
2. ☒ Unexecuted declaration and power of attorney
3. ☒ Informal drawings, 2 sheets (Figs. 1-6)
4. ☒ Information Disclosure Statement

Priority is claimed for this application, corresponding application/s having been filed as follows:

08/09/00
JC839 U.S. PTO

09/09/00 "09/09/00"

Country: Great Britain
Number: 0015236.3
Date: 21 June 2000

Country: Great Britain
Number: 0017726.1
Date: 19 July 2000

The priority documents

☐ are enclosed
☒ will follow.

Date: August 9, 2000

Respectfully submitted,



Melvin C. Garner
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Attorney for Applicant(s)

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TITLE

POWER REDUCTION

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FIELD OF THE INVENTION

The present invention relates to a technique for reducing the maximal signal amplitude and hence the crest factor of a signal, for example, a multi-carrier signal such as a DMT (discrete multi-tone) signal, and particularly but not exclusively to such a signal generated by a modem.

BACKGROUND OF THE INVENTION

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The amplitude distribution of a band-limited, over-sampled signal such as a multi-carrier signal, generated by an ADSL (asymmetric digital subscriber line) modem, can be considered as Gaussian with a zero mean and a variance equal to the power of the transmitter signal. The envelope of such multi-carrier signals randomly present, with a small probability, peaks of a very high amplitude. The span (i.e. the peak to peak voltage) of signals driven by the line driver, which is the component dedicated to transmit the power on the line, is important.

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The power consumption of the line driver is strongly related to the span of the voltage. In order to keep a reasonable level of power consumption, a certain amount of "clipping" of voltage peaks is desired. Such clipping reduces the voltage peaks of the signal.

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However, any such clipping generates broadband noise, usually on the whole bandwidth of the signal. This noise causes loss of data: in the transmit direction by suddenly increasing the background noise; and in the receive direction due to the leakage of the noise through the hybrid. This effect in the receive direction is mainly visible in systems utilising echo cancellers.

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Consequently, too frequent clipping causes degradation of the overall bit error ratio of the transmission, resulting in data being lost. Thus, whilst a small amount of clipping is allowable and tolerable, the probability of clipping occurring must be small enough to ensure good performance, i.e. no significant data loss.

US Patent No. 6,038,261 discloses a method for set-up of a signal in multi-carrier modulation, including clipping of the signal amplitude. A feedback loop is utilised to reinject a clipping noise from a previous pulse into a new pulse. The clipping noise is redistributed outside the useful slip of the signal. This method ensures that the signal never exceeds a predetermined threshold, since the output signal is obtained directly from the output of a clipping circuit. However, the use of a feedback loop makes the behaviour of the technique unpredictable when processing a sequence of pulses. Whilst the technique works in a predictable fashion when processing two successive peaks, feeding the re-distributed noise of the first back to the second, with a sequence of peaks operation would be unstable. With a sequence of peaks it would be difficult to predict the contribution made by successive peaks in the feedback loop, and the system would be too unpredictable for reliable operation.

It is an object of the present invention to provide a technique that reduces the maximum signal amplitude of a multi-carrier signal whilst maintaining the integrity of the signal. In particular the invention aims to reduce the maximum signal amplitude whilst maintaining the bit error ratio of the original multi-carrier signal. Consequently the invention reduces the crest factor of the signal.

SUMMARY OF THE INVENTION

In one aspect the present invention provides a method of processing a band-limited, over-sampled signal comprising:

reducing the amplitude of those portions of the signal having peaks above a threshold value; and controlling the frequency position of the noise associated with the reduction of such peaks. Thereby the crest factor of the signal is reduced.

80 The amplitude of those portions of the signal having peaks above a threshold value are not limited to the threshold value. Whilst they may be reduced to a value at or below the threshold value, they may be reduced to a level which is above the threshold value.

85 The step of reducing the amplitude may comprise: clipping the signal relative to the threshold; filtering the clipped signal; and combining the filtered clipped signal with the band-limited, over-sampled signal. The step of clipping the
90 signal may comprise: limiting the amplitude of the signal to the threshold; and subtracting the limited signal from the band-limited, over-sampled signal.

The step of filtering the clipped signal may comprise creating
95 a pulse having a pre-determined shape dependent upon the clipped samples.

The step of combining may comprise subtracting the filtered
100 signal from the band-limited, over-sampled signal.

The step of combining may comprise delaying the band-limited, over-sampled signal by an amount corresponding to the time taken to implement the clipping and filtering steps.

105 The step of moving the noise may comprise filtering.
The step of controlling the frequency position of the noise, may comprise moving the noise outside the frequency band used by the signal.

110 The step of controlling the frequency position of the noise may comprise moving the noise outside the frequency band used by a signal transmitted in the opposite direction. The signal may contribute an echo to the signal transmitted in the

opposite direction. Although this may result in the clipping noise being present in the used band of the transmitted signal, there may be circumstances where it is more desirable to ensure that no clipping noise contributes to the echo.

In another aspect the present invention provides a method of processing a band-limited, over-sampled signal comprising: clipping the signal at a given threshold; subtracting the clipped signal from the signal; filtering the subtracted signal to thereby control the frequency position of the clipping noise; delaying the signal; and subtracting the filtered signal from the delayed signal, thereby reducing the amplitude of those portions of the signal housing peaks above the threshold.

In a further aspect the invention provides a circuit for processing a band-limited, over-sampled signal, comprising: circuitry for reducing the amplitude of those portions of the signal having peaks above a threshold value; and circuitry for controlling the frequency position of the noise associated with such peaks.

The circuitry for reducing may comprise: a limiter for clipping the signal relative to the threshold; a filter for filtering the clipped signal; and an arithmetic unit for combining the filtered clipped signal with the signal. The arithmetic unit is preferably a subtractor.

The circuit may further include a delay circuit for providing a delayed version of the signal to the arithmetic unit.

The circuitry for controlling the frequency position of the noise preferably comprises a filter. The filter preferably controls the frequency position of the noise to move the noise outside the useful frequency band of the signal.

The circuitry for controlling the frequency position of the noise may control the noise to be positioned outside the

frequency band used by the signal. The circuitry for controlling the frequency position of the noise may control the noise to be positioned outside the frequency band used by a signal transmitted in the opposite direction. The signal may contribute an echo to the signal transmitted in the opposite direction.

In a still further aspect the present invention provides a circuit for processing a band-limited, over-sampled signal comprising: a limiter for clipping the signal to a given threshold; a first subtractor for subtracting the clipped signal from the multi-carrier signal, thereby generating clipped samples; a filter for filtering the clipped samples to thereby control the frequency position of the clipping noise; a delay circuit for generating a delayed version of the multi-carrier signal; and a second subtractor for subtracting the filtered clipped samples from the delayed multi-carrier signal, wherein the amplitude of those portions of the signal having peaks above the threshold is reduced.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention will now be described with regard to a non-limiting example with reference to the accompanying drawings in which:

Figure 1 illustrates a block diagram of a circuit for implementing the technique of the present invention; and

Figures 2 to 6 illustrate waveforms at various points in the circuit of Figure 1 in an illustrative example.

DESCRIPTION OF THE PREFERRED EMBODIMENT

The invention is described herein with reference to a particular example concerning the processing of a signal generated by an ADSL (asymmetric digital subscriber line) modem. Such a modem generates a DMT (discrete multi-tone)

190 signal carrying multiple carrier frequencies. Such signals
are band-limited, over-sample signals. However, the invention
is not limited to the processing of such a signal, and may be
more broadly applied to the processing of any band-limited,
over-sampled signal. The invention is not limited in its
195 applicability to multi-carrier signals, but may also apply to
single carrier signals. The following description uses the
example of a multi-carrier signal for illustrative purposes
only.

200 As discussed in the introduction hereinabove, as a result of
the combining of multiple carriers, the signal at the output
of the modem contains random peaks which are above a desired
threshold. The level of the desired threshold is user or
implementation dependent.

205 Referring to Figure 1, there is illustrated a block diagram of
a digital circuit suitable for processing the multi-carrier
signal at the output of the modem in accordance with a
preferred embodiment of the present invention. The thus
210 processed signal is then suitable for inputting to a line
driver.

The digital circuit, generally designated by reference numeral
12, comprises a clipper or limiter 2, a filter 6, a delay unit
215 10, and two arithmetic or subtractor units 4 and 8.

The input to the digital circuit 12 on line 14 is, in this
illustrative embodiment, the multi-carrier DMT signal
comprising samples from the ADSL modem. The samples from the
220 ADSL modem are over-sampled in order to obtain a band-limited
signal. The samples from the ADSL modem on line 14 form an
input to the limiter 2. Referring to Figure 2, there is
illustrated an example of the samples on line 14 forming an
input to the digital circuit.

225 The limiter 2 clips the samples from the ADSL modem on line 14
at a given threshold and provides clipped samples on line 16.

The threshold applied in the limiter 2 is determined based on the tolerable degradation of the bit error ratio, and will be user or implementation dependent. In general the lower the required bit error ratio, the higher the required threshold. The principles and implementation of such clipping are well-known in the art.

In the present example, it is assumed that the threshold value is a level V_{TH} . The limiter or clipper 2 thus generates a sequence of pulses including four pulses corresponding to the four pulses above the threshold value V_{TH} as shown in Figure 3. As can be seen in Figure 3, the amplitude of these four pulses is limited at the output of the limiter or clipper 2 to the threshold value V_{TH} .

The clipped samples on line 16 are provided to the arithmetic unit 4 together with the samples from the ADSL modem on line 14. The arithmetic unit 4 subtracts the clipped samples on line 16 from the samples from the ADSL modem on line 14, and a sequence of clipped samples is thus generated on line 18, as shown in Figure 4.

Referring to Figure 4, it can be seen that the clipped samples indicate four samples corresponding to the four original samples above the threshold value V_{TH} . The amplitude of these samples corresponds to their amplitude above the threshold value V_{TH} .

Thus the subtraction performed by the subtractor 4 extracts the part of the samples of the incoming signals which are above the threshold.

The sequence of clipped samples on line 18 are then provided as an input to the filter 6. The filter 6 filters the clipped samples on line 18 to shape the noise generated by the clipping operation. The filter computes a pulse from the clipped samples which has a spectrum outside the signal bandwidth of the useful signal. Thus, in this preferred

embodiment, the filter outputs a sequence of samples which have a spectrum outside the used band of the multi-carrier ADSL signal generated by the modem.

270 This type of processing, where the samples are moved out of band, is known as soft-clipping. By contrast, hard-clipping would result in the samples (and thus the noise) remaining in band. Soft-clipping techniques using a filter are well-known to one skilled in the art. Similarly the use of filters
275 generally to control the frequency position of a signal is well-known.

In this preferred embodiment the filtering is carried out using a Finite Impulse Response Filter.

280 Although in this preferred embodiment the filter is utilised to ensure the noise is out of band, in general it would be possible to derive from the clipped samples a pulse with a "good" spectrum. However, without shifting the noise outside
285 the used frequency band good performance is not achieved in the illustrative embodiment described herein.

In general terms, the filter controls the frequency position of the noise associated with the peaks above the threshold
290 value. In certain applications, it may be advantageous to control the frequency position within the useful band of the band-limited, over-sampled signal.

In one example of such a scenario, the noise associated with
295 the clipping of the peaks above the threshold is controlled so as to be outside the useful band of the signal received in the modem. As is well-known in the art, the signal received in the modem is susceptible to echo effects due to the signal transmitted in the same modem, the echo effect being a
300 characteristic of a hybrid used to transmit and received signals on the communication channel. By ensuring that the clipping noise is moved outside the useful band of any signal received in the hybrid of the modem, then any clipping noise

leakage in the echo is reduced. Although this may result in
 305 the clipping noise being present in the used band of the
 transmitted signal, there may be circumstances where it is
 more desirable to ensure that no clipping noise contributes to
 the echo.

310 In the present illustrative embodiment, and in most
 applications, it is preferable to move this noise outside the
 useful band.

Thus, in the illustrative embodiment the filter provides a
 315 modified version of the clipped samples in the incoming signal
 in another frequency spectrum. The shape of the filtered
 clipped samples is shown in Figure 5.

The sequence of filtered clipped samples generated by the
 320 filter 6 are provided on line 20 to the arithmetic unit 8.
 The arithmetic unit 8 also receives on line 24 a delayed
 version of the input signal on line 14. The delayed signal is
 provided by the delay unit 10. The delay is provided to
 325 compensate for the computation time in the limiter 2 and the
 filter 6, such that the timing of the delayed version (or
 replica) of the input signal coincides with the pulses at the
 output of the filter. The filtered clipped samples on line 20
 are subtracted from the delayed version of the input signal on
 line 24 in the subtractor 8. This subtraction removes the
 330 pulse, generated from the clipped samples, from the incoming
 signal.

The waveform resulting from this subtraction is illustrated in
 Figure 6. As can be seen from Figure 6, although the signal
 335 still has peaks above the threshold value V_{TH} , the peaks of the
 signal which originally exceeded V_{TH} are reduced with respect
 to the original signal. The peak voltage and the power
 consumption of the signal provided to the line driver on line
 22 are thus decreased. Although Figure 6 illustrates that
 340 none of the peaks are actually reduced below V_{TH} , those peaks

which are initially only a certain level above V_{TH} may be reduced to a level at or below V_{TH} .

The crest factor of the final signal output by the digital
345 circuit 12 on line 22 depends on three factors:

1. The maximal crest factor of the input signal on line 14;
2. The threshold value used in the limiter 2; and
3. The way in which the pulse is derived from the clipped
350 samples in the filter 6.

There is an obvious relationship between the first two factors
and the final crest factor: the higher the maximal input, the
higher the maximal output; and the higher the threshold value,
355 the higher the maximal output.

As regards the filter, optimisation of the design to adjust
the final maximal crest factor will be dependent upon the
design of the filter itself. The design of the filter is
360 outside the scope of the present invention, but will be
readily realised by one skilled in the art.

Thus the present invention provides a technique in which any
peaks above a threshold level are reduced, but not clipped,
365 such that the effects of such peaks is reduced. Although the
implementation of the technique preferably includes a clipping
step, it is performed on the front-end rather than as the last
step in the technique, such that the output signal is not a
clipped signal. Any noise introduced by the clipping step,
370 so-called clipping noise, is preferably filtered out of the
useful frequency band of the signal.

Although the invention is described herein with respect to
processing samples in the digital domain, the principles of
375 the invention apply also to the analogue domain.

WHAT IS CLAIMED IS:

380

1. A method of processing a band-limited, over-sampled signal comprising: reducing the amplitude of those portions of the signal having peaks above a threshold value; and controlling the frequency position of the noise associated

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2. The method of claim 1 wherein the step of reducing the amplitude comprises: clipping the signal relative to the threshold; filtering the clipped signal; and combining the filtered clipped signal with the band-limited, over-sampled signal.

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3. The method of claim 2 wherein the step of clipping the signal comprises: limiting the amplitude of the signal to the threshold; and subtracting the limited signal from the band-limited, over-sampled signal.

395

4. The method of claim 2 wherein the step of filtering the clipped signal comprises creating a pulse having a pre-determined shape dependent upon the clipped samples.

400

5. The method of any of claims 2 to 4 wherein the step of combining comprises subtracting the filtered signal from the band-limited, over-sampled signal.

405

6. The method of claim 2 wherein the step of combining comprises delaying the band-limited, over-sampled signal by an amount corresponding to the time taken to implement the clipping and filtering steps.

410

7. The method of claim 1 wherein the step of moving the noise comprises filtering.

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8. The method of claim 1 wherein the step of controlling the frequency position of the noise comprises moving the noise outside the frequency band used by the signal.

420 9. The method of claim 1 wherein the step of controlling the frequency position of the noise comprises moving the noise outside the frequency band used by a signal transmitted in the opposite direction.

425 10. The method of claim 9 wherein the signal contributes an echo to the signal transmitted in the opposite direction.

430 11. A method of processing a band-limited, over-sampled signal comprising: clipping the signal at a given threshold; subtracting the clipped signal from the signal; filtering the subtracted signal to thereby control the frequency position of the clipping noise; delaying the signal; and subtracting the filtered signal from the delayed signal, thereby reducing the amplitude of those portions of the signal having peaks above the threshold.

440 12. A circuit for processing a band-limited, over-sampled signal, comprising: circuitry for reducing the amplitude of those portions of the signal having peaks above a threshold value; and circuitry for controlling the frequency position of the noise associated with the reduction of such peaks.

445 13. The circuit of claim 12 wherein the circuitry for reducing comprises: a limiter for clipping the signal relative to the threshold; a filter for filtering the clipped signal; and an arithmetic unit for combining the filtered clipped signal with the signal.

450 14. The circuit of claim 13 wherein the arithmetic unit is a subtractor.

15. The circuit of claim 12 further including a delay circuit for providing a delayed version of the signal to the arithmetic unit.

455

16. The circuit of claim 12 wherein the circuitry for controlling the frequency position of the noise comprises a filter.

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17. The circuit of claim 12 wherein the circuitry for controlling the frequency position of the noise controls the noise to be positioned outside the frequency band used by the signal.

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18. The circuit of claim 12 wherein the circuitry for controlling the frequency position of the noise controls the noise to be positioned outside the frequency band used by a signal transmitted in the opposite direction.

470

19. The circuit of claim 18 wherein the signal contributes an echo to the signal transmitted in the opposite direction.

475

20. A circuit for processing a band-limited, over-sampled signal comprising: a limiter for clipping the signal to a given threshold; a first subtractor for subtracting the clipped signal from the multi-carrier signal, thereby generating clipped samples; a filter for filtering the clipped samples to thereby control the frequency position of the clipping noise; a delay circuit for generating a delayed version of the multi-carrier signal; and a second subtractor for subtracting the filtered clipped samples from the delayed multi-carrier signal, wherein the amplitude of those portions of the signal having peaks above the threshold is reduced.

485

490

[Figure 1]

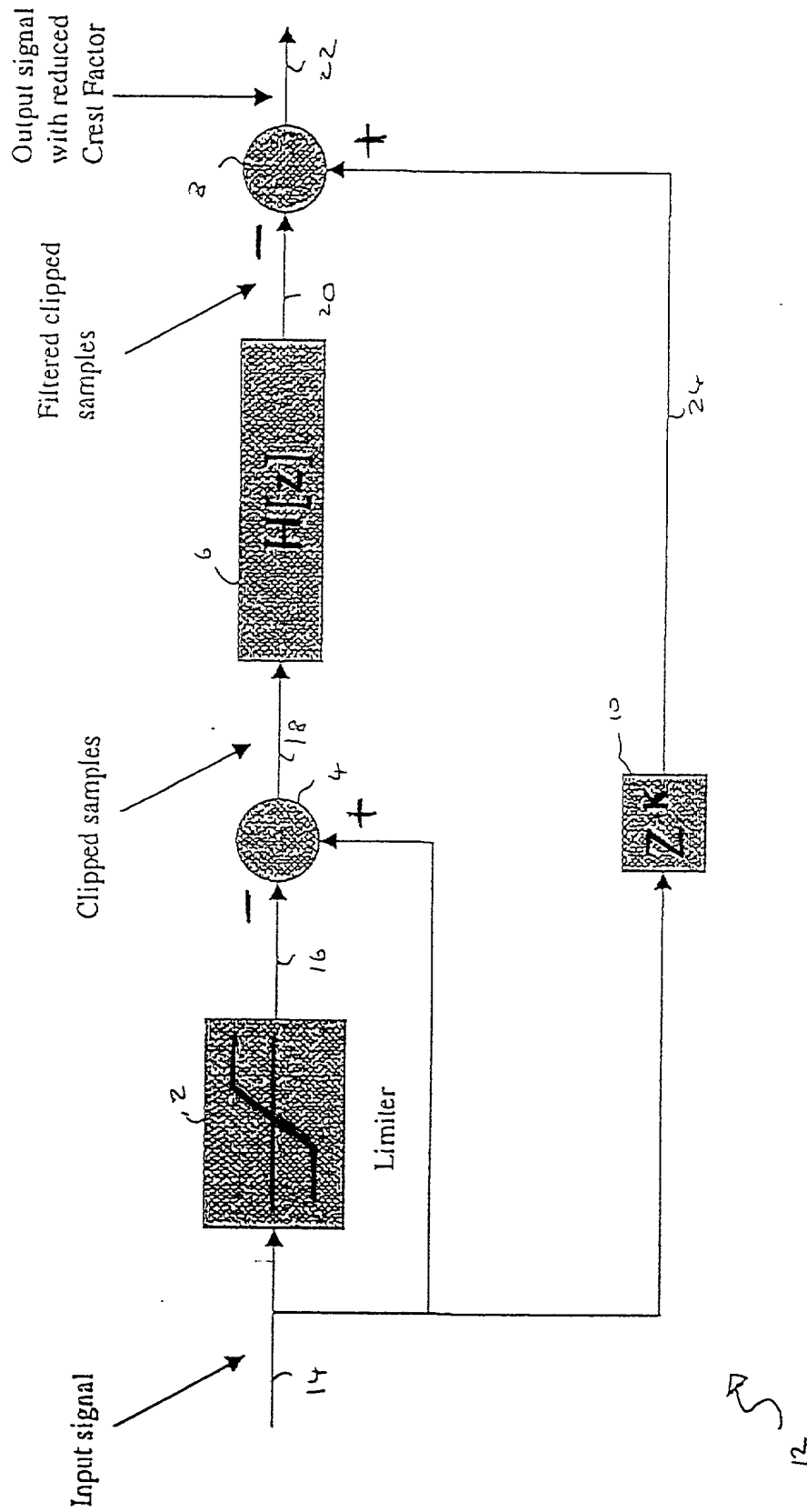


Fig 1

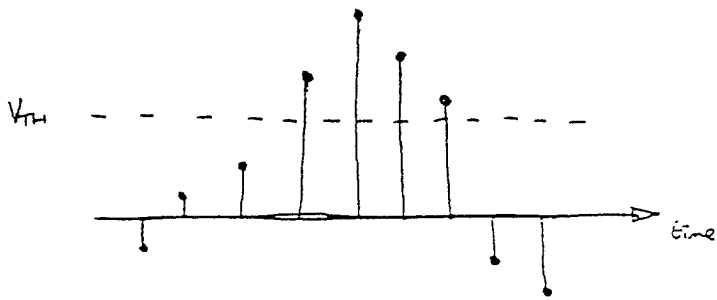


Fig 2.

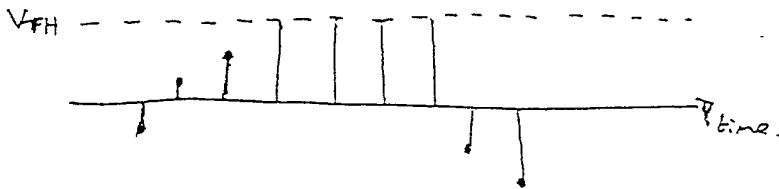


Fig 3

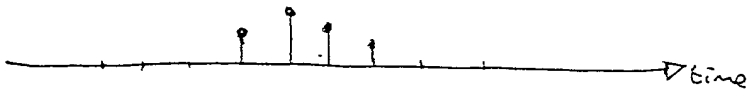


Fig 4



Fig 5

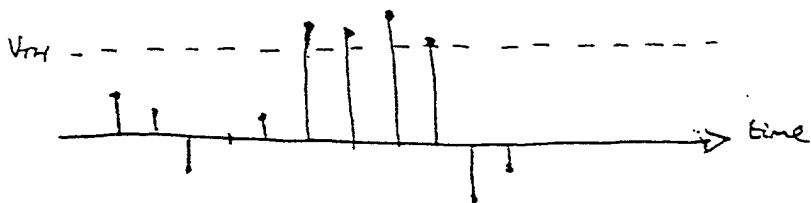


Fig 6

005080 05246353

05

**DECLARATION
AND POWER OF ATTORNEY
Original Application**

As a below named inventor, I declare that the information given herein is true, that I believe that I am the original, first and sole inventor if only one name is listed at 1 below, or a joint inventor if plural inventors are named below, of the invention entitled:

POWER REDUCTION

which is described and claimed in:

[X] the attached specification or [] the specification in application
Serial No. , filed
(for declaration not accompanying appl.)

that I do not know and do not believe that the same was ever known or used in the United States of America before my or our invention thereof or patented or described in any printed publication in any country before my or our invention thereof, or more than one year prior to this application, or in public use or on sale in the United States of America more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months prior to this application, that I acknowledge my duty to disclose information of which I am aware which is material to patentability in accordance with 37 CFR §1.56. I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I hereby claim the priority benefits under 35 U.S.C. §119 of any application(s) for patent or inventor's certificate listed below. All foreign applications for patent or inventor's certificate on this invention filed by me or my legal representatives or assigns prior to the application(s) of which priority is claimed are also identified below.

PRIOR APPLICATION(S), IF ANY, OF WHICH PRIORITY IS CLAIMED

<u>COUNTRY</u>	<u>APPLICATION NO.</u>	<u>DATE OF FILING</u>
Great Britain	0015236.3	21 June 2000
Great Britain	0017726.1	19 July 2000

**ALL FOREIGN APPLICATIONS, IF ANY, FILED PRIOR
TO THE APPLICATION(S) OF WHICH PRIORITY IS CLAIMED**

<u>COUNTRY</u>	<u>APPLICATION NO.</u>	<u>DATE OF FILING</u>
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POWER OF ATTORNEY:

As a named inventor, I hereby appoint the following attorney(s) and/or agents(s) to prosecute this application and transact all business in the Patent and Trademark office connected therewith: Gordon D. Coplein #19,165, William F. Dudine, Jr. #20,569, Michael J. Sweedler #19,937, S. Peter Ludwig #25,351, Paul Fields #20,298, Marc S. Gross #19,614, Joseph B. Lerch #26,936, Melvin C. Garner #26,272, Ethan Horwitz #27,646, Beverly B. Goodwin #28,417, Adda C. Gogoris #29,714, Martin E. Goldstein #20,869, Bert J. Lewen #19,407, Henry Sternberg #22,408, Peter C. Schechter #31,662, Robert Schaffer #31,194, Robert C. Sullivan, Jr. #30,499, Ira J. Levy #35,587, Joseph R. Robinson #33,448, Scott G. Lindvall #40,325

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POST OFFICE ADDRESS: Avenue de Castel, 73 CITY: Brussels STATE OR COUNTRY: BELGIUM ZIP CODE: 1200

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

SIGNATURE OF INVENTOR 1: _____ DATED: _____